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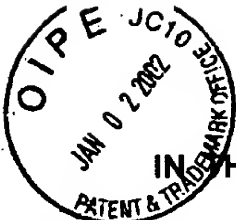
<b>TRANSMITTAL FORM</b> <i>(to be used for all correspondence after initial filing)</i>	Application Number	09/651,422
	Filing Date	August 30, 2000
	First Named Inventor	J. Honeycutt
	Group Art Unit	2815
	Examiner Name	A. Wilson
Total Number of Pages in This Submission	Attorney Docket Number	MI22-1332

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SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT	
Firm or Individual name	David G. Latwesen, Ph.D. #38,533 Wells, St. John, Roberts, Gregory & Matkin, P.S.
Signature	
Date	11/6/01

CERTIFICATE OF MAILING	
I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, Washington, DC 20231 on this date: 11/6/01	
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. 09/651,422  
Filing Date August 30, 2000  
Inventor Jeffrey W. Honeycutt et al.  
Assignee Micron Technology, Inc.  
Group Art Unit 2815  
Examiner A. Wilson  
Attorney's Docket No. MI22-1332  
Title: Transistor Structures, Methods of Forming Transistor Structures, and  
Methods of Forming Insulative Material Against Conductive Structures

**RESPONSE TO OCTOBER 16, 2001 OFFICE ACTION**

To: Box NON-FEE AMENDMENT  
Assistant Commissioner for Patents  
Washington, D.C. 20231

From: David G. Latwesen, Ph.D. (Tel. 509-624-4276; Fax 509-838-3424)  
Wells, St. John, Roberts, Gregory & Matkin P.S.  
601 West First Avenue, Suite 1300  
Spokane, WA 99201-3828

Responsive to the Office Action dated October 16, 2001, Applicant amends  
and remarks as follows:

**AMENDMENTS**  
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**In the Claims**

✓  
Cancel claims 21-31.